

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,088,056 B2
APPLICATION NO. : 10/506445
DATED : August 8, 2006
INVENTOR(S) : Toshiaki Kurachi et al.

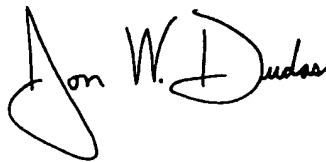
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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column	Line	Claim	Description
12	21	1	Before "protrusion". insert --a--
12	27	2	Delete "part" and insert --an upper end--
12	28	2	After "bulb", delete remainder of claim and insert --in such a manner as to lift the luminous bulb in a direction opposite to the base, thereby allowing the connection wire to be disposed spaced apart from the sealing portion.--
12	38	4	After "wherein the", delete remainder of claim and insert --connection wire and the sealing portion are spaced apart from each other by 0.3 mm or more.--
12	41	5	After "wherein", delete remainder of claim and insert --the greatest length of the circuit board is 60 mm or less.--
12	43	6	After "lamp", delete remainder of claim and insert --of claim 1, wherein a phosphor or a protective coating is not applied to an inner wall of the sealing portion.--
13	41	8	After "circuit", insert --board--
14	34	9	Delete "claim 6" and insert --claim 8--
14	37	10	Delete "claim 6" and insert --claim 8--
14	40	11	Delete "claim 6" and insert --claim 8--

Signed and Sealed this

Twenty-sixth Day of December, 2006



JON W. DUDAS
Director of the United States Patent and Trademark Office